



000174-0308-101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Vaughn Betz (As Amended)  
Application No. : 10/796,502 Confirmation No.: 3014  
Filed : March 8, 2004  
For : SYSTEMS AND METHODS FOR REDUCING  
STATIC AND TOTAL POWER CONSUMPTION IN  
PROGRAMMABLE LOGIC DEVICE  
ARCHITECTURES  
Group Art Unit : 2116  
Examiner : Michael J. Brown

New York, New York 10020  
October 30, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

REPLY TO OFFICE ACTION

Sir:

Responsive to the Office action mailed  
September 28, 2006, please amend the above-identified  
patent application as follows:

Amendments to the Specification begin on page 2  
of this Reply.

Amendments to the Claims begin on page 3 of  
this Reply.

Remarks begin on page 7 of this Reply.